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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Lehmann, *et al.* Docket No.: 00P0113 US
Serial No.: 09/751,474 Filed: December 28, 2000
Patent No.: 7,087,975 B2 Issue Date: August 8, 2006
For: Area Efficient Stacking of Antifuses in Semiconductor Device

Certificate of Mailing via First Class Mail (37 C.F.R. § 1.8(a))

Date of Deposit: September 15, 2006

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Certificate of Mailing via First Class Mail (1 page)
Request for Issuance of Certificate of Correction (1 page)
Certificate of Correction (1 original and 1 copy)
Return Postcards (2)

Respectfully submitted,

Michelle Reyes
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Legal Assistant

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Certificate
SEP 20 2006
of Correction

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Patentee: Lehmann, *et al.* Docket No.: 00P0113 US
Serial No.: 09/751,474 Art Unit: 2811
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Filed: December 28, 2000 Examiner: Steven Ho Yin Loke
For: Area Efficient Stacking of Antifuses in Semiconductor Device

Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Request for Issuance of Certificate of Correction
Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322

Dear Sir:

Upon review of the above-identified issued patent, Patentee notes an error in the claims that should be corrected as shown on the attached Certificate of Correction.

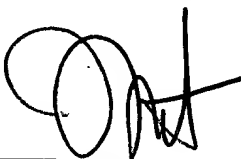
The mistake noted on the attached Certificate of Correction was incurred through the fault of the Patent and Trademark Office. Accordingly, no fee is required.

Patentee respectfully solicits the issuance of the requested Certificate of Correction.

Respectfully submitted,

9/15/06

Date



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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,087,975 B2

APPLICATION NO. : 09/751,474

ISSUE DATE : August 8, 2006

INVENTOR(S) : Lehmann, *et al.*

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 27, line 8; delete "electrical, state" insert --electrical state,--

MAILING ADDRESS OF SENDER(Please do not use customer number below):

Slater & Matsil, L.L.P.
17950 Preston Road, Suite 1000
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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application for to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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